

Cross-CUT Interference Present in Timing Extraction

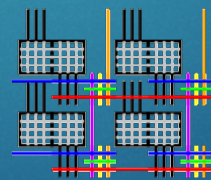
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Implementation of Computation

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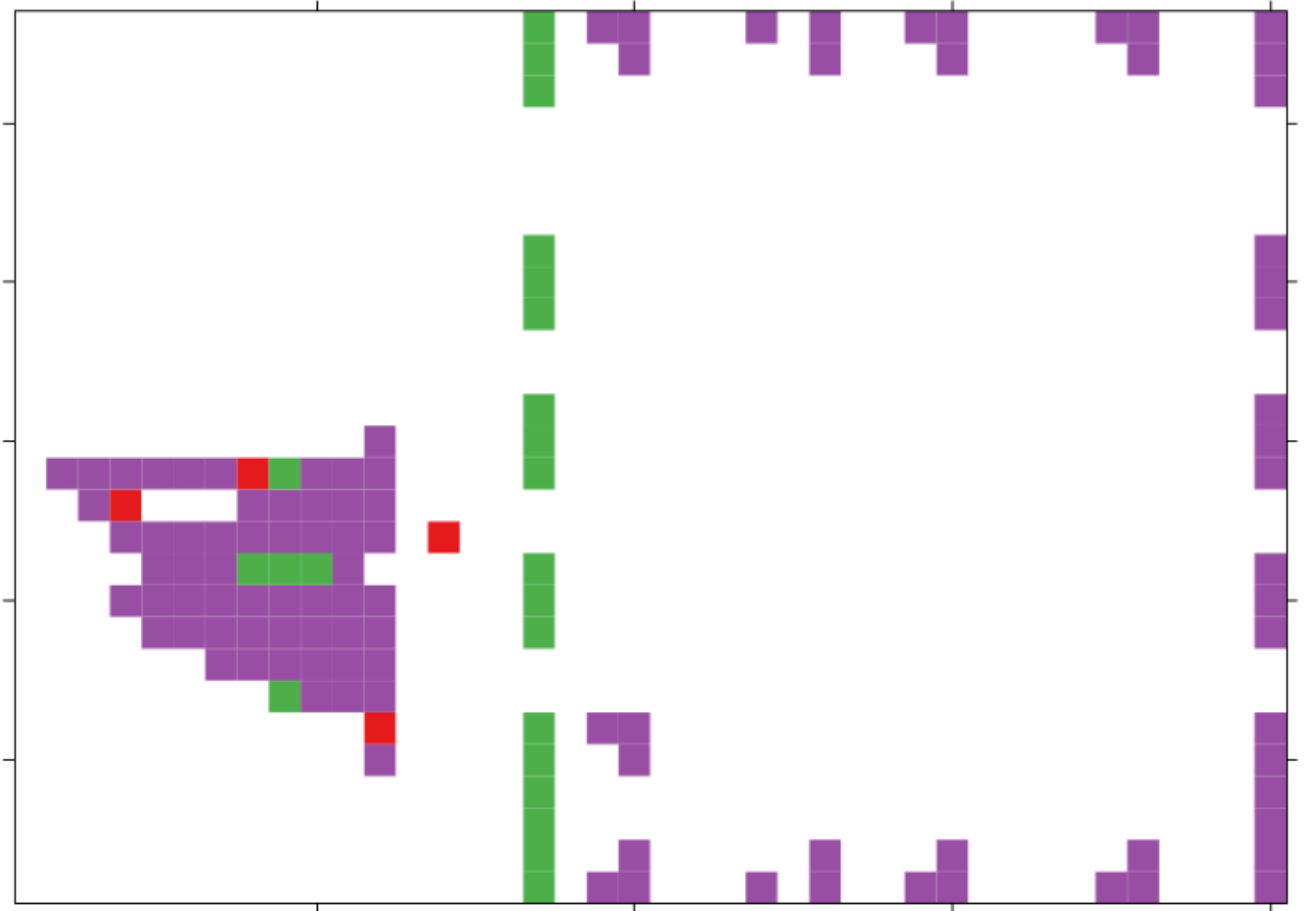
Group

What's an FPGA?

- ▶ Reconfigurable Computer Chip
- ▶ Uses:
 - ▶ DSP
 - ▶ Computer Vision
 - ▶ Network Control

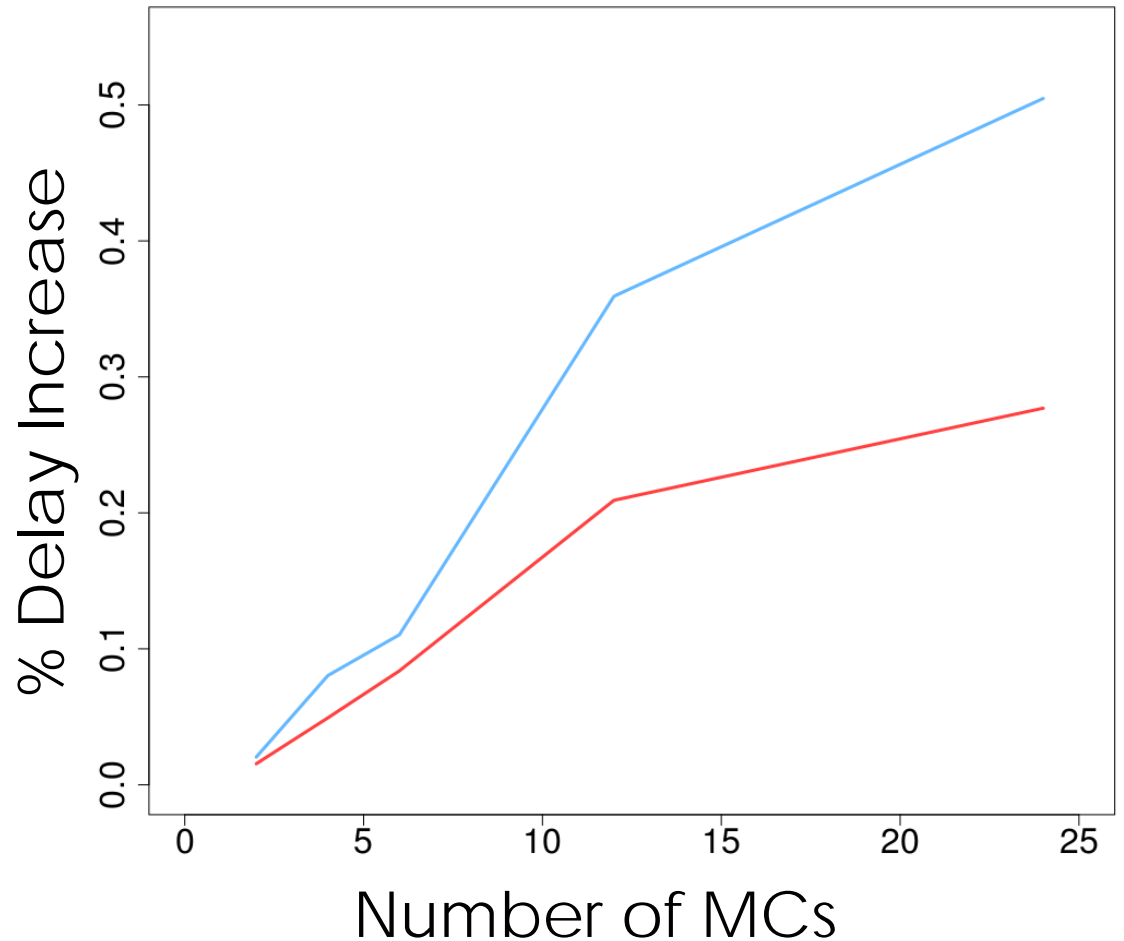
Timing a Path

- ▶ No two FPGAs have the same path delays!
- ▶ Test each path individually with a measurement circuit



There's a Problem...

- ▶ Adding measurement circuits increases the load on the clock
- ▶ Loading the clock slows everything else down



...And a Solution

- ▶ Placing MCs in different quadrants reduces interference
- ▶ Keep only one MC active at a time

